

**REMARKS**

Claims 1-52 are pending in the present application.

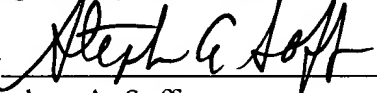
Claim 1 has been amended prior to examination.

Attached hereto is a marked-up version of the changes made to the specification and claims by the current amendment. The attached page is captioned "Version with markings to show changes made."

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to pass this application to issue.

Dated: July 18, 2002

Respectfully submitted,

By 

Stephen A. Soffen

Registration No.: 31,063

DICKSTEIN SHAPIRO MORIN &  
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant

**Version With Markings to Show Changes Made**

1. (Amended) An input/output controller integrated circuit comprising:

a host interface subsystem to couple to a host to receive host commands and to transceive data blocks with the host in response to the host commands; and

a mapping controller coupled to the host interface subsystem, the mapping controller to map logical block addresses of the host commands into [physical] block addresses of one or more peripherals to transceive the data blocks with the one or more peripherals.